

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: **Kazuto IKEDA**

Serial Number: **Not Yet Assigned**

Filed: **October 16, 2003**

**Customer No.: 38834**

For: **METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE HAVING  
SILICIDE LAYER**

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

October 16, 2003

Sir:


In compliance with 37 CFR 1.56, Applicant calls to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge

Deposit Account No. 50-2866.

Respectfully submitted,  
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Enclosures: PTO-1449; References (4)

<b>INFORMATION DISCLOSURE STATEMENT PTO-1449</b>	Atty. Docket No. 032016	Serial No. New Appln.
	Applicant(s): Kazuto IKEDA	
	Filing Date: October 16, 2003	Group Art Unit:

### U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

### FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Translation (Yes or No)	
_____	AE	11-111642	04/23/99	Japan	Abstract. Discussed in the spec.
_____	AF	11-233456	08/27/99	Japan	Abstract. Discussed in the spec.
_____	AG				
_____	AH				
_____	AI				

### OTHER DOCUMENTS

_____	AJ	Ken INOUE et al.; IEDM 95-445, pp. 18.1.1 to 18.1.4, 1995. Discussed in the spec.
_____	AK	K. GOTO et al.; IEDM 95-449, pp. 18.2.1 to 18.2.4, 1995. Discussed in the spec.

Examiner	Date Considered
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